



Shri Phanishwar Nath Renu Engineering College, Araria

(Under Department of Science Technology and Technical Education Patna, Govt. of Bihar)

Ref. No: SPNREC/890
Room No: 06

Date: 23/08/2024
W.E. F: 02/09/2024

Time Table, SPNREC ARARIA

4th SEM, Electronics & Communication Engineering

	Period 1	Period 2	Period 3		Period 4	Period 5	Period 6
Day	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00-3:00	3:00-4:00	4:00-5:00
Mon	Analog Circuit	Library		LUNCH BREAK	Library		
Tue	Analog Communication	Analog Circuit	Semiconductor physics and devices		Library		SMY
Wed	Analog Circuit	Semiconductor physics and devices	Electromagnetic Theory		Library	Digital Circuit Lab	
Thu	Digital Circuit	Electromagnetic Theory	Electromagnetic Theory		SMY	Analog Circuit Lab	
Fri	Digital Circuit	Analog Communication	Semiconductor physics and devices		SMY	Semiconductor physics and devices Lab	
Sat	Digital Circuit	Analog Communication	Library		Analog Communication Lab		

Course Title	Faculty
Digital Circuit(T+P)	Prof. Shaheen, Asst. Prof ECE
Analog Circuit(T+P)	Prof. Dharmendra Kumar Asst. Prof ECE
Semiconductor physics and devices	Prof. Amit Ranjan, Asst. Prof ECE
Analog Communication(T+P)	Prof. Dr. Ritesh Kumar Asst. Prof ECE
Electromagnetic Theory	Prof. Rajkumar Sharma / Prof. Priyatam Kumar Asst. Prof ECE
SMY	Prof. Amit Ranjan/Prof. Priyatam Kumar Asst. Prof ECE
Open Elective	Prof. Dharmendra Kumar, Asst. Prof ECE

Prof. I/C
21/08/24

Professor I/C
Time Table
(ECE Dept.)

F
Prof. I/C
21/08/24

HOD (ECE Dept.)
SPNREC, ARARIA

S. Passwar
21/08/2024

Professor I/C
Time Table
SPNREC, ARARIA

Dean
21/08/24

Dean (Academic)
SPNREC, ARARIA

Principal
21/08/24

Principal
SPNREC, ARARIA